



Research Article

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Implementation and design of power amplifier module with momentum

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ABSTRACT

Power amplifier modules are key components of RF communication systems. This paper describes the implementation of a 1.95 GHz power amplifier module. The design of micro-strip matching circuit in modules and methods of momentum simulation are described. The test results show that the module features excellent performance, satisfying requirements of the design.

**Key words:** Power amplifier module; Micro-strip; matching circuit; Momentum simulation

INTRODUCTION

Power amplifier modules that are the key device in modern wireless communications are highly applied in such systems as radar, navigation, electronic countermeasures and satellite communications. PA with the high efficiency, high output power, high linearity and high gain can be broadly applied in the fields like digital transmitter mobile communication base station and military technology[1-3]. The 3G and 4G technology set the needs for far better performance of the RF microwave PA in order to satisfy the needs for the real application in Broad Band, efficiency, output power and linearity. The L waveband, used as a universal waveband, is a carrier for many systems. So the device and module on this waveband is extensively used.

THE DESIGN OF THE MATCHING CIRCUIT

The matching circuit is a key link in the design of PA. Inputting matching circuit is mainly for accomplishment of the wider broadband and higher power gain while outputting matching circuit is mainly for accomplishment of high work efficiency and power delivery. When designing the network of the matching circuit, the designers should concern the simplex of the circuit, the width of the waveband and the category and the adjustability of it. Comprehensively, a kind of mingled matching circuit network of micro-strip line and discrete component is adopted in this paper[4-5]. The micro-strip line, a popular plane transmission line recently, is processed through Gerber and easily integrated with other wave devices passive and active. The characteristic resistance value of the micro-strip line is decided by the parameters of width, the thickness and base materials of base plate, the thickness of the copper foil and so on. The characteristic resistance value is decided by the width only if the materials are sure. According to the formula:

$$Z_0 = \frac{Z_f}{\sqrt{\epsilon_{eff} [1.393 + w/h + 0.667 \ln(w/h + 1.444)]}} \quad (1)$$

Computing the width W, h in the formula is the thickness of the base

$$\epsilon_{eff} = \frac{\epsilon_r + 1}{2} + \frac{\epsilon_r - 1}{2} \left(1 + 12 \frac{w}{h}\right) \quad (2)$$

$$Z_f = \sqrt{\frac{\mu_0}{\epsilon_0}} = 376.8\Omega \tag{3}$$

In order to minimize the energy loss in the signal transmission, the inputting and outputting signal connecting line matches with the front and back grades through the 50 Ohm characteristic impedance. The matching circuit is realized by the ADS simulation software, adopted the load-pull to reach the best load impedance in the PA module.

The principle of the load-pull is that under the stimulation of the large-signal level the PA tests the outputting power through the successive load changes, diagramming the curve of the equal-gain and the equal-power in the Smith diagram, which design accurately through the election of the proper output impedance. The load-pull can reach the best load resistance value precisely to simulate large-signal PA characters such as the output rate, gain, additional power and the IMD3 and IP3[6]. Comprehensive concern on the best load resistance value through the load-pull in figure 1,  $Z_L=6.507+j*5.791\text{Ohm}$  and the best origin resistance value  $Z_S=1.89-j*5.599\text{Ohm}$  which both are .Used in the matching network design.

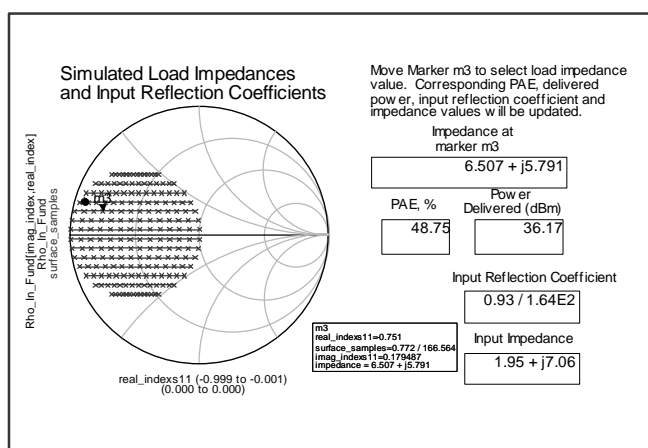


Fig. 1:the best load resistance value from load-pull

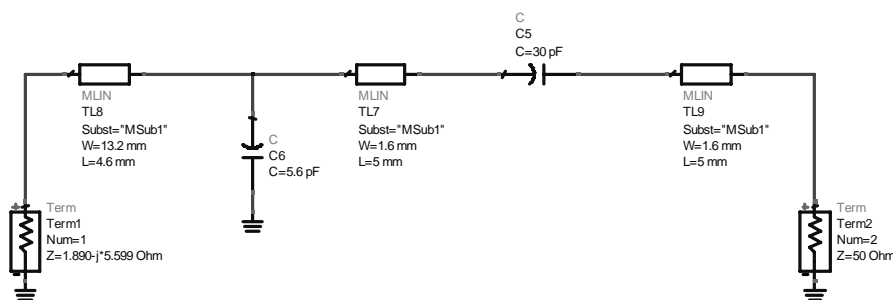


Fig.2: Input matching circuit

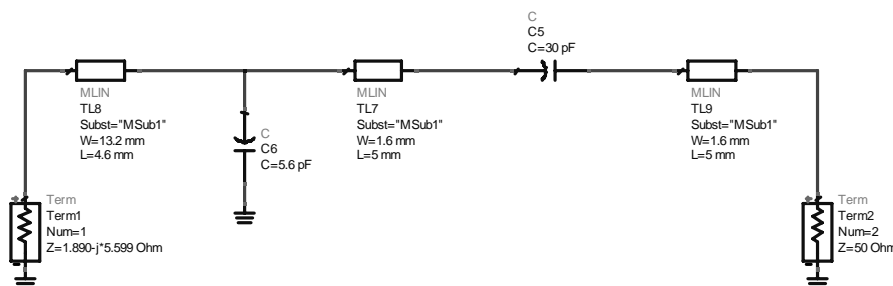


Fig.3: Output matching circuit

**THE MODULE CHOOSES THE FREE SCALE CHIP MW6S004NT1**

Concerning on the module miniaturization and high performance, a kind of mingled matching circuit network of micro-strip line and discrete component is adopted while the medium base plate uses RO4350B from the ROGERS Company whose relative base material is 3.48. And the thickness of the base plate substrate is 0.8mm while the base

plate copper foil is 0.035mm. All the procession of the matching is finished in the Smith diagram, and fully take advantages of the Q to design the waveband needed, and the consequential impute and output matching circuit separately are figure 2 and 3:

**THE MOMENTUM DESIGN OF PA**

Schematic circuit diagram is simulated under completely ideal conditions, while there is a considerable gap between the real production of circuit board and its theory. Thus, this needs to consider the effects of the factors such as interference coupling. So the domain needs to be further simulated. ADS domain adopts momentum to simulate in electromagnetic way [7]. The simulation result is more accurate than in the schematic diagram.

Agilent momentum is a leading three-dimension EM plane emulator, which can be used to model and analyze for passive circuit and can simulate any design of geometric structure(including multilayered structure) and can adopt MOM technical and accurately-simulate complex electromagnetic effect(including coupling and parasitism). The exact electromagnetic simulation can support designers improving design performance of RF/MMIC RF/high speed PCB RF/SIP and ANT, in addition, it ensures that the products can achieve technical indicator .momentum combine full-wave static electromagnetism solver to use for RF passive device and high frequent interconnection and parasitic modeling [8]. Its high efficient reseat and self-adaptive frequency sampling and NLOGN solver can shorten time and can simulate complex electromagnetic effect; including skin effect subtract effect heavy metal and more dielectric effect. Transfer matching network into momentum element for circuit design, then combine biasing circuit to get the circuit of PA module. As shown in figure 4

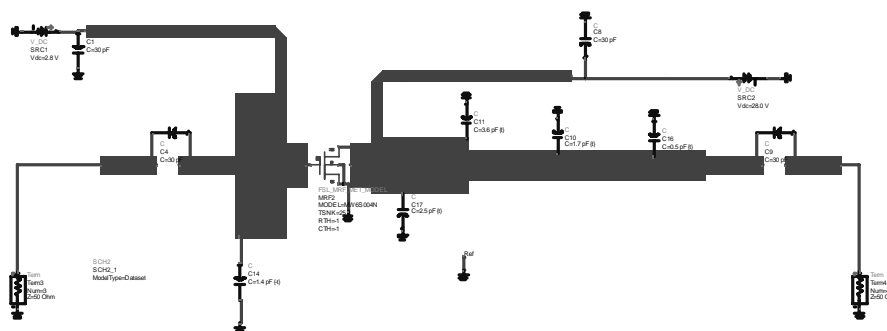


Fig.4: momentum design drawing of PA

Adopt single-tone harmonic balance method to simulate circuit. Set drain voltage : 28V grid voltage 2.8v operating frequency 1.95GHZ scanning power 0~25dbm simulation result as shown in figure 5. It can be seen from figure that it meets the design requirement, output power of comprehension point of 1db circuit is about 36dbm, gain is about 20db PAE is about 40%

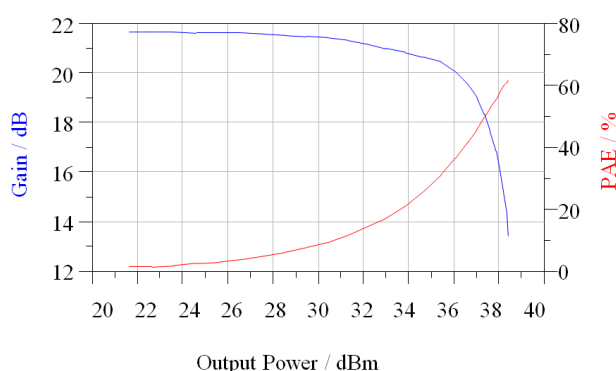


Fig.5: relation curve between output power and gain and output

Simulate circuit in daphnia harmonic balance method, result is shown in figure 6. When the output is 36m, IMD3i is about -30bc, with a good performance.

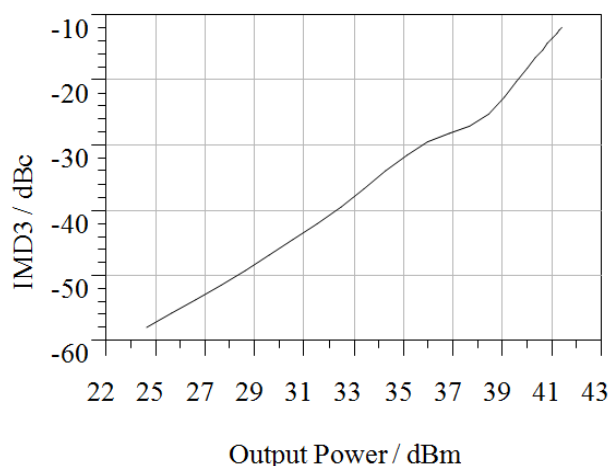


Fig.6: curve relation between IMD3 and output power

### TEST RESULT

According to momentum artificial circuit which processes module, through repeated debugging, then test it .Test instrument employs new vector network analyzer of Agilent company-PNA-XM5241A. At last get the gain curve, as shown in figure 6.it can be seen that the central frequency in 1.95GHZ of small-signal gain is 19.7db, which matches with simulation curve well. Figure 7 is curve of IM3 module, and the curve show in the frequency range 1.9GHZ~2.1GHZ.while,it has a good linearity with IM3 below -31db

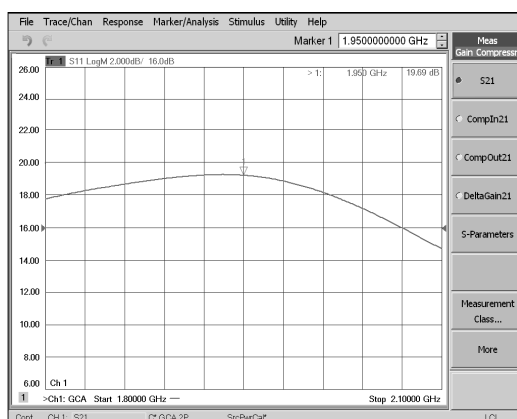


Fig.7: Gain curve of PA

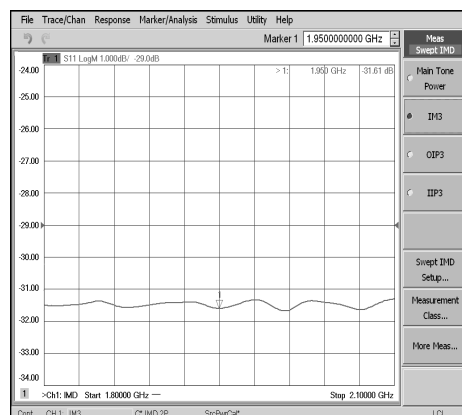


Fig.8: Im3 curve of PA

### CONCLUSION

Pa is a most common power module in RF circuit, which plays a decisive role in communication system design. The

method momentum improves design of pa from simple circuit level to physical level, and the result is more accurate. Momentum run through all the design process, Using Circle diagram to Conjugate match input and output impedance, making full use of Q circle to design bandwidth, and getting the high-performance PA module. Test result indicates that output power of 1db is about 36dbm,PAE is approximant to 40%,the gain of central frequency in 1.90GHz is about 20db,the gain in the 1.8GHz~2.1GHz frequency band range hold above 15db, while IM3 below-31db.the outcome of test and simulation match well and it verify design correctness.

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